## Wrap-up

CSCI 237: Computer Organization 36<sup>th</sup> Lecture, Friday, December 5, 2025

**Kelly Shaw** 

Slides originally designed by Bryant and O'Hallaron @ CMU for use with Computer Systems: A Programmer's Perspective, Third Editio

1

### Last Time

- ■Exceptional Control Flow
- Exceptions
- **■**Computer chips

**Administrative Details** 

- Lab #6 due today at 5pm
- Colloquium today at 2:35pm
- Xiwei Xuan, UC Davis
- Toward Responsible AI through Efficient Data Science and Learning
- "Sample" final available on Glow (from S18, not all questions are relevant)
- Extra practice problems added to webpage (solns on Glow)
- Review session
  - Friday, Dec. 12, 5-6pm
- I'm available next Wednesday/Thursday/Friday email me
- Final exam
- Sunday, December 14 at 01:30 PM in Clark Hall 105

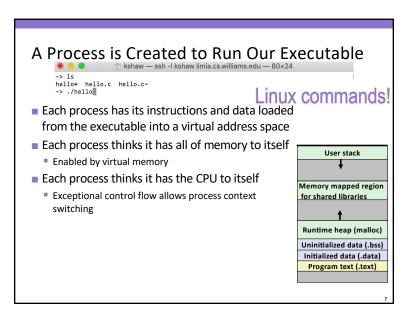
2



2

# Start by Writing a Program in C | Star

5



Compile it to an Executable

gcc! make!

- Learned how to represent data
  - Twos complement
- Floating point
- Learned how to manipulate data
  - Logical and bitwise manipulation
  - Arithmetic operations (including shifting for ÷ and × )
- How to convert C code to assembly code
- Registers/memory
- if/else, for/while/do-while, switch
- Function calls
- Layout of arrays and structs in memory
- How executables are created
- Text segment holds instructions
  - Encoded in binary (Y86-64)
- Data segments hold global data

6



- PC register is set with address of first instruction
- Register storing page table address is set
- Instructions executed on pipelined datapath
  - Stalling or data forwarding pipeline used depending on processor
  - Branch prediction used to reduce control hazards
  - Every virtual address (instructions and data) translated to physical address and sent to memory hierarchy
    - Access TLB, page table, potentially causes page fault
    - Physical addresses sent to L1/L2/DRAM

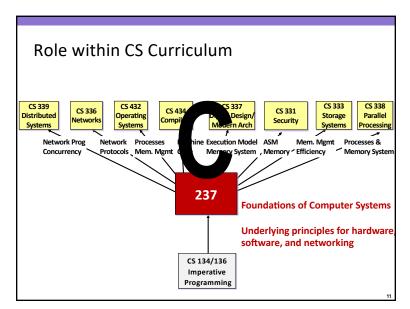
7

## Process Starts Running (2)

halto halto

- Operating system invoked on traps/faults/syscalls/interrupts
  - Examples: grow heap, page fault, timer interrupt, divide by 0, read virtual address 0
  - If instruction causes exception, flag is set and carried down pipeline until instruction reaches WB stage
    - Process state (i.e. registers, condition codes) saved into memory
    - Exception table used to find starting address in kernel of exception handler
    - Handler code starts running on processor
- Calls to dynamic memory allocator just normal execution of instructions in library code until system calls to grow heap invoke OS

9



Role within CS Curriculum CS 339 CS 338 CS 432 CS 337 CS 333 CS 336 CS 434 CS 331 Distributed Operating Digital Design Storage Parallel Compilers Security Systems Modern Arch Systems Machine Execution Model ASM Network Processes Mem. Mgmt Processes & Concurrency Protocols Mem. Mgmt Code Memory System , Memory - Efficiency 237 **Foundations of Computer Systems Underlying principles for hardware** software, and networking CS 134/136 Imperative Programming

10

### **Final Exam**

- Closed book, closed notes
- Cumulative with emphasis on material not tested on yet
- Will provide info w/ instructions and registers similar to midterm
- Things to think about
  - Midterm, quizzes, practice problems, in-lecture questions
  - Short answer conceptual questions

### Thank You!

13

- This is a tough course with a **lot** of material to learn.
- Be proud of all you accomplished! You learned lots of conceptual knowledge and lots of skills. Both will serve you well in future classes and beyond.
- Thanks for being so great throughout!
- It's been fun getting to know you! Please don't be strangers.



# **Student Course Surveys**

14

To access the online evaluations, log into **Glow** (glow.williams.edu) using your regular Williams username and password (the same ones you use for your Williams email account). On your Glow dashboard you'll see a course called "**Course Evaluations**." Click on this and then follow the instructions you see on the screen. If you have trouble finding the evaluation, you can ask a neighbor for help or reach out to ir@williams.edu.