

Machine Level Programming: Procedure Calls including Recursion

CSCI 237: Computer Organization
17th Lecture, Friday, Oct. 17

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Administrative Details

- Read CSAPP 3.8
- Weekly quiz open is due today at 2:30
- Midterm in lab on Wednesday 10/23
 - Closed book and closed notes
 - Review session on Monday 10/21 7-8:30pm in Physics 205
 - Sample midterm and solutions on Glow
- Apply to be a TA!
- TA feedback forms
- CS Colloquium talk today at 2:35pm in Wege
 - What I did last summer part 2

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Last Time: Machine-Level Programming: Procedures

- Procedures
 - Stack Structure
 - Calling Conventions
 - Passing control
 - Passing data
 - Managing local data
 - Register saving conventions
 - Illustration of Recursion

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Today: Machine-Level Programming: Procedures and Arrays

- Procedures
 - Stack Structure
 - Calling Conventions
 - Passing control
 - Passing data
 - Managing local data
 - Register saving conventions
 - Illustration of Recursion

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Procedure Data Flow (Review)

Registers

- First 6 arguments

%rdi
%rsi
%rdx
%rcx
%r8
%r9

- Return value

%rax

Stack

...
Arg n
...
Arg 8
Arg 7

- Only allocate stack space when needed (e.g., more than 6 integral arguments)!

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Data Flow Examples

```
void multstore
(long x, long y, long *dest)
{
    long t = mult2(x, y);
    *dest = t;
}
```

```
0000000000400540 <multstore>:
# x in %rdi, y in %rsi, dest in %rdx
...
400541: mov    %rdx,%rbx    # Save dest
400544: callq 400550 <mult2> # mult2(x,y)
# t in %rax
400549: mov    %rax,(%rbx)  # Save at dest
...
```

```
long mult2
(long a, long b)
{
    long s = a * b;
    return s;
}
```

```
0000000000400550 <mult2>:
# a in %rdi, b in %rsi
400550: mov    %rdi,%rax    # a
400553: imul   %rsi,%rax    # a * b
# s in %rax
400557: retq               # Return
```

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Today: Machine-Level Programming: Procedures and Arrays

- Procedures
 - Stack Structure
 - Calling Conventions
 - Passing control
 - Passing data
 - Managing local data
 - Register saving conventions
 - Illustration of Recursion

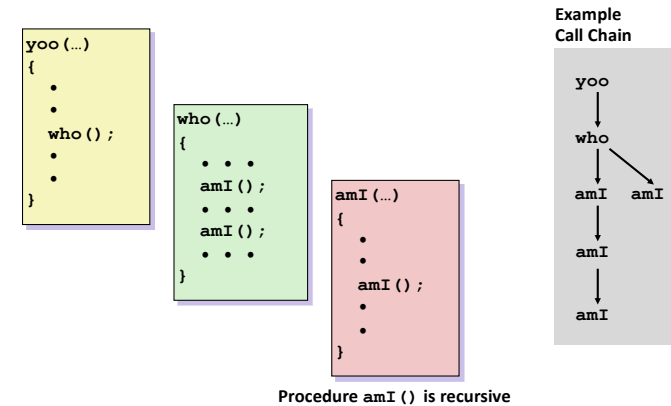
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Stack-Based Languages

- Languages that support recursion
 - e.g., C, Java
 - Code must be “Reentrant”
 - There can be multiple simultaneous instantiations of a single procedure
 - We need some place to store the **state** of each instantiation
 - Arguments
 - Local variables
 - Return address
- Stack discipline
 - The state for a given procedure is only needed for a limited time
 - From when the procedure is called to when the return occurs
 - **Callee** returns before **caller** does
- Stack allocated in **Frames**
 - A frame stores the state for single procedure instantiation

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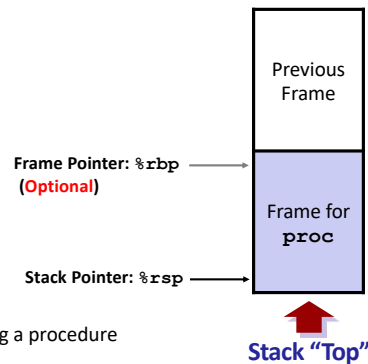
Call Chain Example



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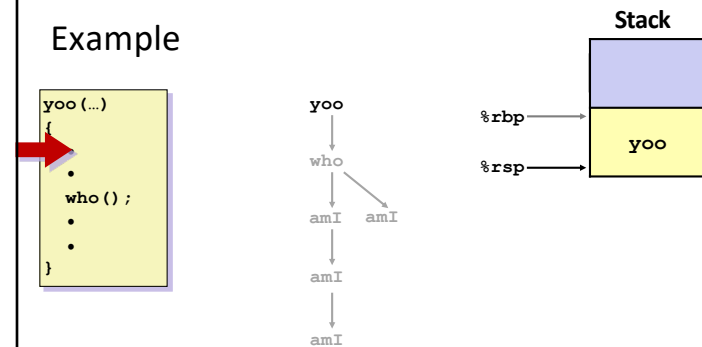
Stack Frames

- Contents:
 - Return information
 - Local storage (if needed)
 - Temporary space (if needed)
- Management
 - Space is allocated when entering a procedure
 - “Set-up” code
 - Includes push by **call** instruction
 - Space is deallocated when a return occurs
 - “Finish” code
 - Includes pop by **ret** instruction

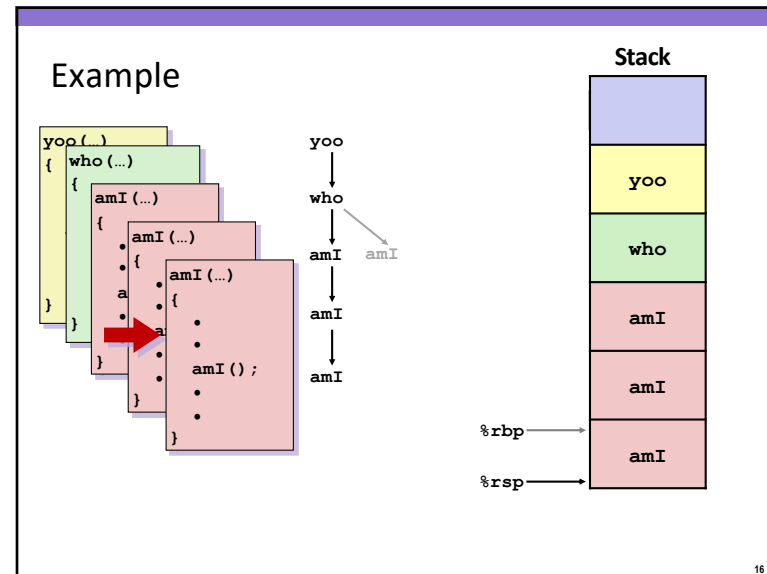
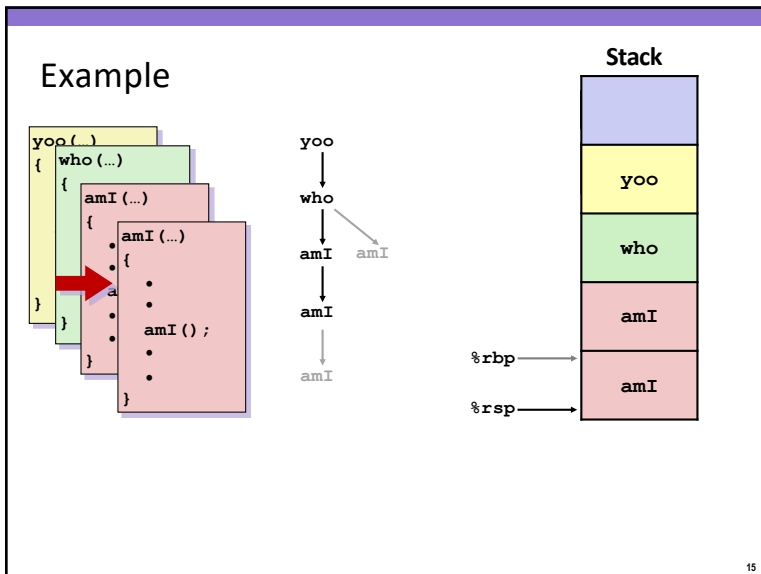
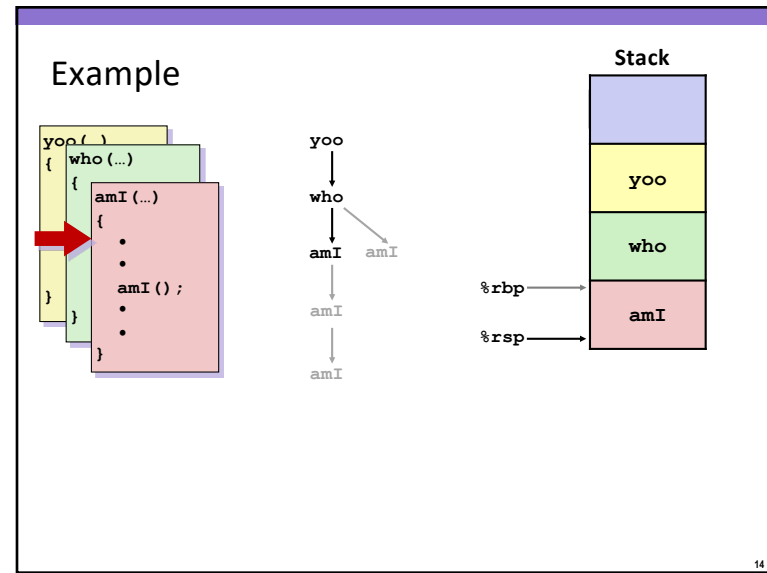
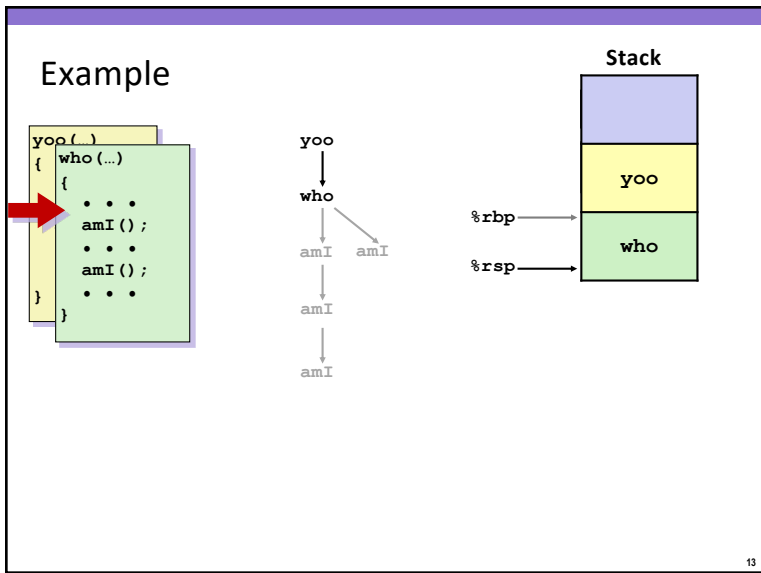


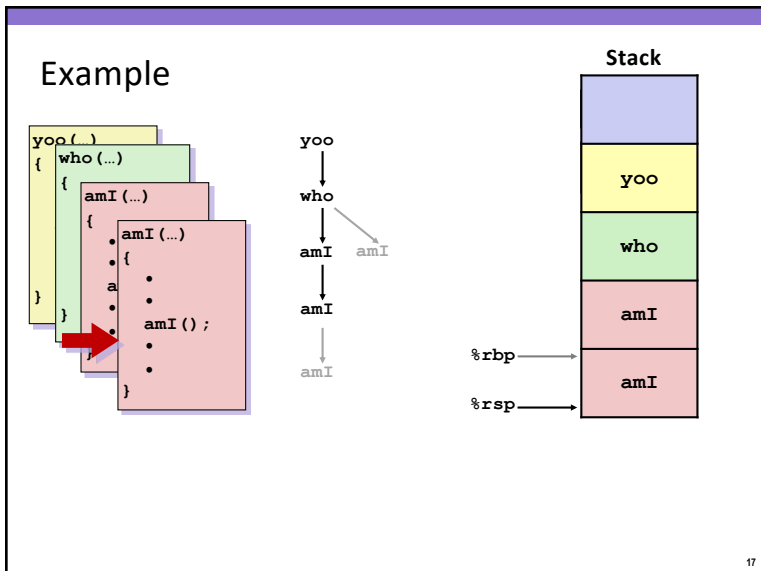
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Example

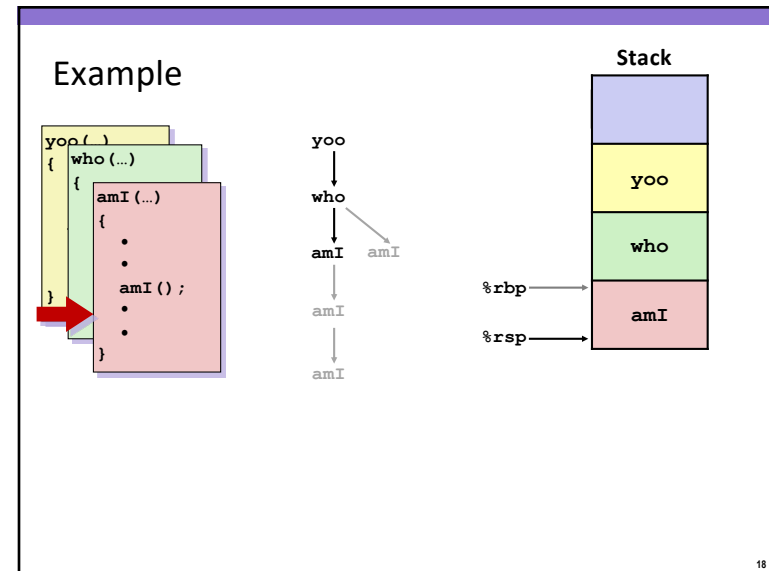


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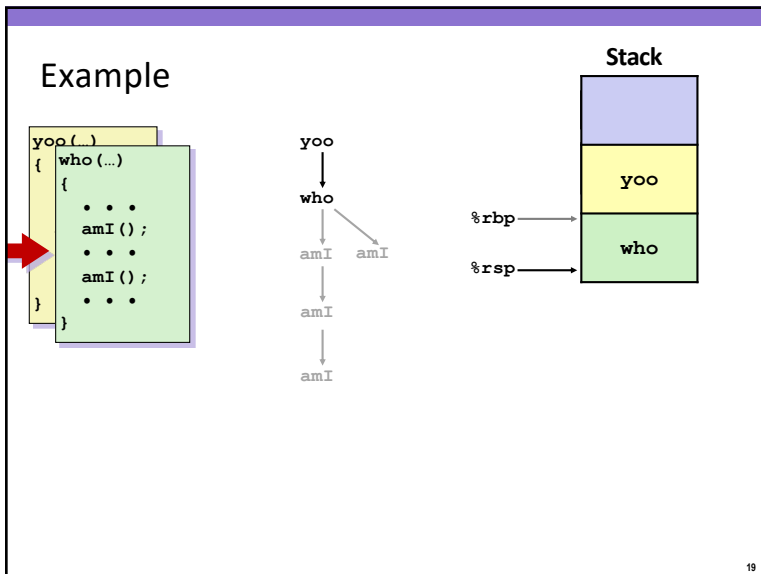




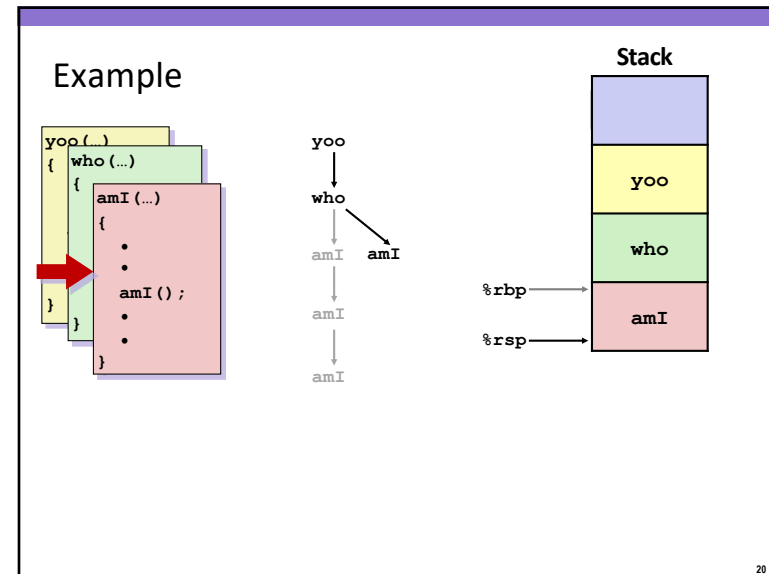
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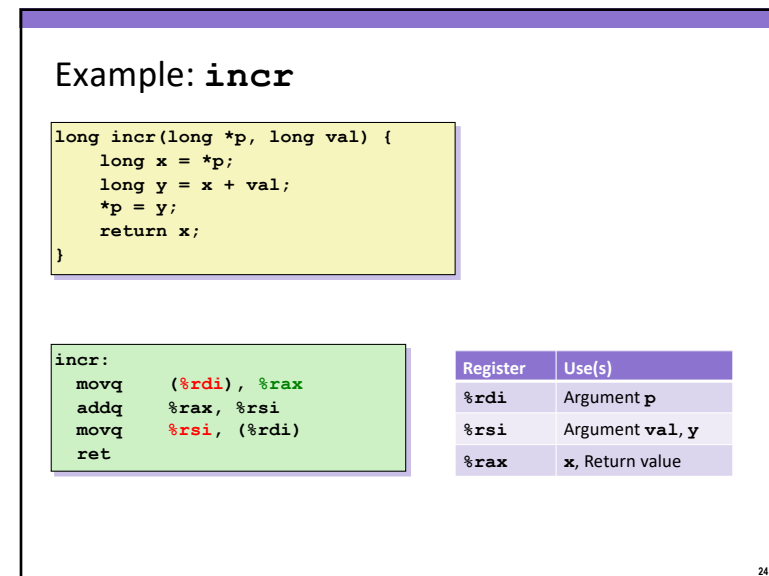
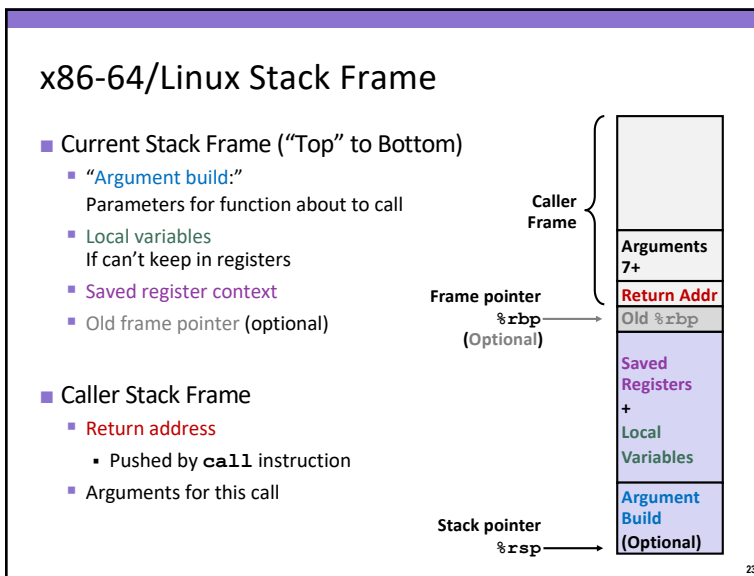
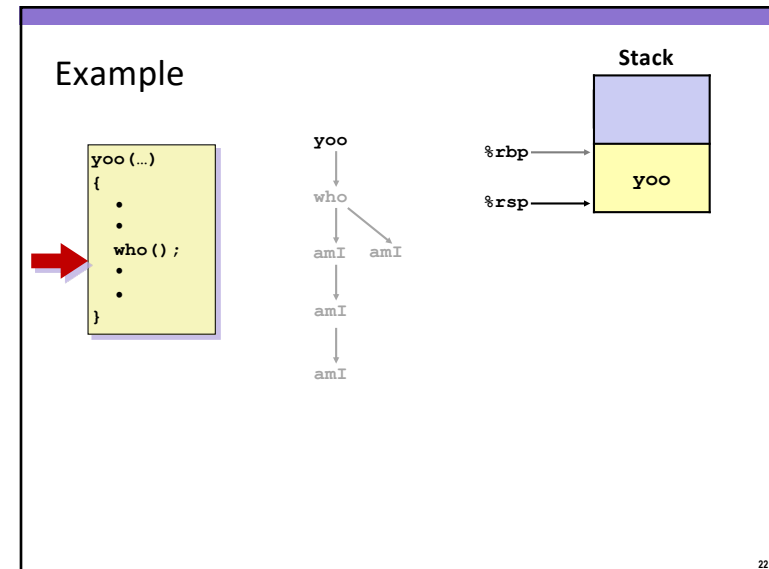
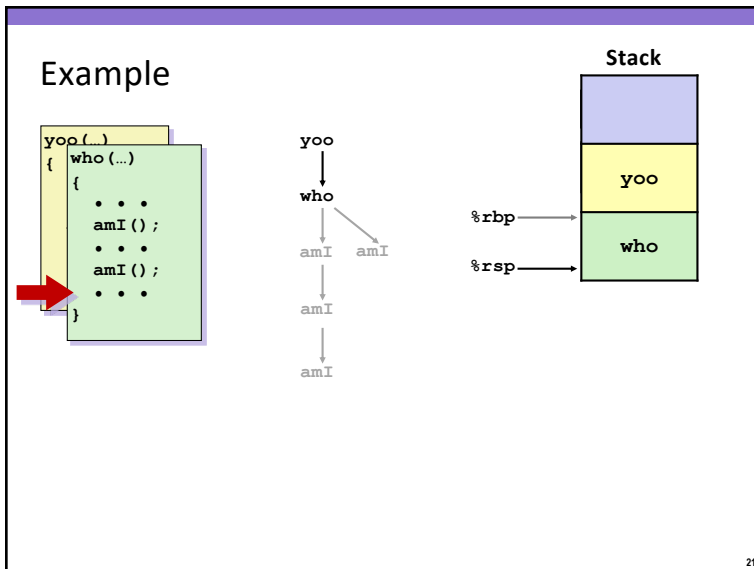
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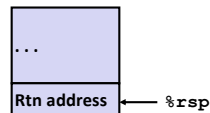
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Example: Calling `incr` #1

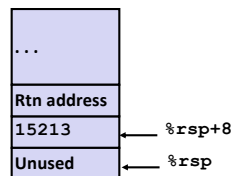
```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

Initial Stack Structure



```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Resulting Stack Structure

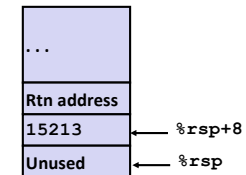


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Example: Calling `incr` #2

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

Stack Structure



```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

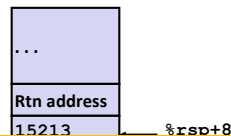
Register	Use(s)
%rdi	&v1
%rsi	3000

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Example: Calling `incr` #2

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

Stack Structure



Aside 1: `movl $3000, %esi`

- Remember, `movl` -> %eax zeros out high order 32 bits.
- Why use `movl` instead of `movq`? 2 bytes shorter.

```
movl    $3000, %esi
leaq    8(%rsp), %rdi
call    incr
addq    8(%rsp), %rax
addq    $16, %rsp
ret
```

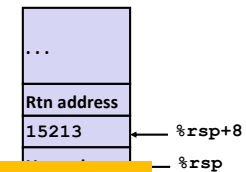
Register	Use(s)
%rdi	&v1
%rsi	3000

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Example: Calling `incr` #2

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

Stack Structure



Aside 2: `leaq 8(%rsp), %rdi`

- Computes `%rsp+8`
- Actually used for what it is meant!

```
leaq    8(%rsp), %rdi
call    incr
addq    8(%rsp), %rax
addq    $16, %rsp
ret
```

Register	Use(s)
%rdi	&v1
%rsi	3000

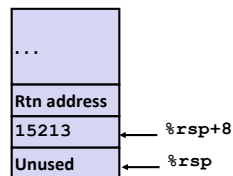
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Example: Calling `incr` #2

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Stack Structure



Register	Use(s)
%rdi	&v1
%rsi	3000

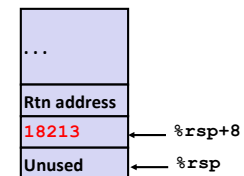
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Example: Calling `incr` #3

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Stack Structure



Register	Use(s)
%rdi	&v1
%rsi	3000, 18213
%rax	15213

```
incr:
    movq    (%rdi), %rax
    addq    %rax, %rsi
    movq    %rsi, (%rdi)
    ret
```

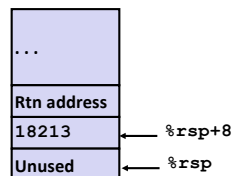
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Example: Calling `incr` #4

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Stack Structure



Register	Use(s)
%rax	33426 (Return value)

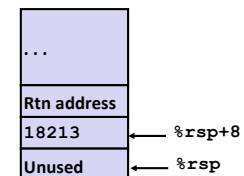
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Example: Calling `incr` #5a

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

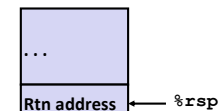
```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Stack Structure



Register	Use(s)
%rax	33426 (Return value)

Updated Stack Structure

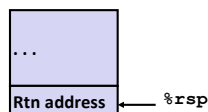


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Example: Calling `incr` #5b

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

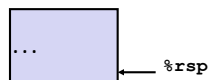
Updated Stack Structure



```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Register	Use(s)
%rax	33426 (Return value)

Final Stack Structure



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Example: Calling `incr` #5b

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

Updated Stack Structure



```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Why not just push \$15213 onto stack? `pushq` allocates 8 bytes, not 16. (GCC tries to align stack frames to 16 bytes by default.)



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Practice on Your Own

- Convert the following x86-64 assembly code to C.

```
fcn:
    leaq    (%rdi, %rsi, 8), %rdi
    pushq   %rdi
    callq   calc
    popq    %rdi
    movq    %rax, (%rdi)
    movq    $1, %rax
    retq
calc:
    movq    (%rdi), %rdi
    addq    %rdi, %rdi
    movq    %rdi, %rax
    retq
```

Given how `calc` is written, was pushing `%rdi` onto the stack required?

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Today: Machine-Level Programming: Procedures and Arrays

- Procedures
 - Stack Structure
 - Calling Conventions
 - Passing control
 - Passing data
 - Managing local data
 - Register saving conventions
 - Illustration of Recursion

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Register Saving Conventions

- When procedure **yoo** calls **who**:

- yoo** is the **caller**
- who** is the **callee**

- Can register be used for temporary storage?

<pre> yoo: . . . movq \$15213, %rdx call who addq %rdx, %rax . . . ret </pre>	<pre> who: . . . subq \$18213, %rdx . . . ret </pre>
---	--

- Contents of register **%rdx** overwritten by **who**
- This could be trouble → something should be done!
 - Need some coordination

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Register Saving Conventions

- When procedure **yoo** calls **who**:

- yoo** is the **caller**
- who** is the **callee**

- Can register be used for temporary storage?

- Conventions

- "Caller Saved"
 - Caller saves temporary values in its frame before the call
- "Callee Saved"
 - Callee saves temporary values in its frame before using
 - Callee restores them before returning to caller

For this class, conventions will be followed strictly.

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x86-64 Linux Register Usage #1

- %rax**

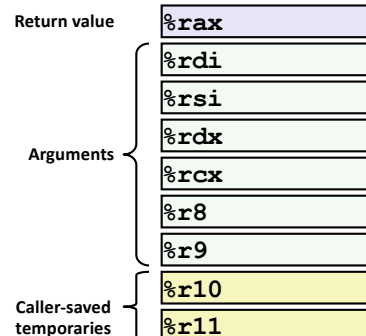
- Return value
- Also caller-saved
- Can be modified by procedure

- %rdi, ..., %r9**

- Arguments
- Also caller-saved
- Can be modified by procedure

- %r10, %r11**

- Caller-saved
- Can be modified by procedure



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x86-64 Linux Register Usage #2

- %rbx, %r12, %r13, %r14**

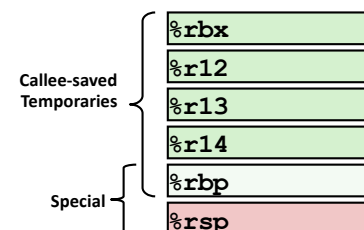
- Callee-saved
- Callee must save & restore

- %rbp**

- Callee-saved
- Callee must save & restore
- May be used as frame pointer
- Can mix & match

- %rsp**

- Special form of callee save
- Restored to original value upon exit from procedure



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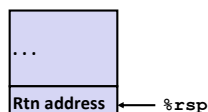
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Callee-Saved Example #1

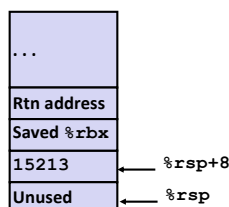
```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

```
call_incr2:
    pushq    %rbx
    subq     $16, %rsp
    movq     %rdi, %rbx
    movq     $15213, 8(%rsp)
    movl     $3000, %esi
    leaq     8(%rsp), %rdi
    call     incr
    addq     %rbx, %rax
    addq     $16, %rsp
    popq     %rbx
    ret
```

Initial Stack Structure



Resulting Stack Structure



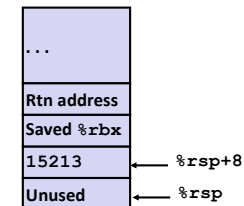
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Callee-Saved Example #2

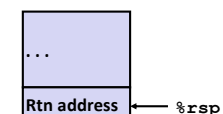
```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

```
call_incr2:
    pushq    %rbx
    subq     $16, %rsp
    movq     %rdi, %rbx
    movq     $15213, 8(%rsp)
    movl     $3000, %esi
    leaq     8(%rsp), %rdi
    call     incr
    addq     %rbx, %rax
    addq     $16, %rsp
    popq     %rbx
    ret
```

Resulting Stack Structure



Pre-return Stack Structure



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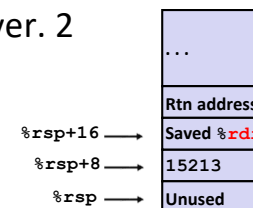
Using Callee-Saved Registers vs. Stack

- Suppose you have a value in a **caller-saved** register X but need to make a function call
- If you don't need the value in X after the function call, don't do anything
- If you do need the value in X after the function call, you need to either:
 - Store value in X on to the stack and then restore value to register from the stack after function call
 - Store value in **callee-saved** register Y to stack, copy X to register Y, use Y after the function call returns, restore original value of Y by copying value from stack to register Y before returning from your function (previous example)

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Callee-Saved Example #1 ver. 2

```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```



```
call_incr2:
    pushq    %rbx
    subq     $16, %rsp
    movq     %rdi, %rbx
    movq     $15213, 8(%rsp)
    movl     $3000, %esi
    leaq     8(%rsp), %rdi
    call     incr
    addq     %rbx, %rax
    addq     $16, %rsp
    popq     %rbx
    ret
```

```
call_incr2:
    pushq    %rdi
    subq     $16, %rsp
    movq     $15213, 8(%rsp)
    movl     $3000, %esi
    leaq     8(%rsp), %rdi
    call     incr
    movq     16(%rsp), %rdi
    addq     %rdi, %rax
    addq     $16, %rsp
    popq     %rdi
    ret
```

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Practice on Your Own

- Which values in fcn() would need to be stored either in callee-saved registers or on the stack?

```
long fcn(long a, long *b, long c)
{
    long tmp = a + c;

    long result = foo(tmp);

    *(b+1) = result - a;

    return tmp;
}
```

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